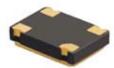
# **M2 Series**

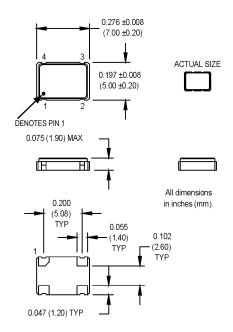
# 5x7 mm, 3.3 Volt, HCMOS/TTL Compatible Output, Clock Oscillator



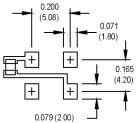








#### SUGGESTED SOLDER PAD LAYOUT



NOTE: A capacitor of value 0.01 μF or greater between Vdd and Ground is recommended.

### **Pin Connections**

PIN	FUNCTION			
1	N/C or Tristate			
2	Ground			
3	Output			
4	+Vdd			

	M2	1	3	Т	С	00.0000 MHz
Product Series ——						
Temperature Range		_				
1: 0°C to +70°C			.			
3: -55°C to +105°C						
<b>5</b> : 10°C to 125°C <b>7</b> : 0°C to 85°C	6: -20°C to +	70°C				
Stability —						
3: ±100 ppm						
<b>5</b> : ±35 ppm						
*8: ±20 ppm	e. Ere ppin					
Output Type ———						
F: Fixed Q: Star	dbv Function	T: 1	Tristat	te		
Symmetry/Logic Com A or G: 40/60 @ 50° C: 45/55 HCMOS	npatibility —— % Vdd**					
Package/Lead Config	urations ——					

<sup>\*</sup>Contact Factory for Availability
\*\* A and G codes are used interchangeably on the M2 Series

	PARAMETER	Symbol	Min.	Тур.	Max.	Units	Condition/Notes		
	Frequency Range	F	1.5		135	MHz	See Note 1		
	Operating Temperature	TA	(See ordering information)						
	Storage Temperature	Ts	-55		+125	°C			
	Frequency Stability	ÄF/F	(See ordering information)						
	Aging								
	1 <sup>st</sup> Year			±3		ppm			
	Thereafter (per year)			±2		ppm			
	Input Voltage	Vdd	3.0	3.3	3.6	٧			
	Input Current	ldd			10	mA	1.500 to 20.000 MHz		
v					20	mA	20.001 to 50.000 MHz		
Specifications					30	mA	50.001 to 67.000		
					55	mA	67.001 to 135.000 MHz		
	Standby Current				10	μΑ	"Q" Output Type		
ĕ	Output Type						HCMOS/TTL Compatible		
	Load		2 TTL or 15 pF			See Note 2			
	Symmetry (Duty Cycle)		(See ordering information)				½ Vdd		
븅	Logic "1" Level	Voh	90% Vdd			V	HCMOS Load		
Electrical			Vdd -0.5			٧	TTL Load		
	Logic "0" Level	Vol			10% Vdd	V	HCMOS Load		
					0.5	V	TTL Load		
	Output Current				± <b>4</b>	mA			
	Rise/Fall Time	Tr/Tf					See Note 3		
					6	ns	1.500 to 50.000 MHz		
					4	ns	50.001 to 80.000 MHz		
					2	ns	80.001 to 135.000 MHz		
	Standby/Tristate Function				ating: output				
			Input Logic "0"; output disables to high-Z						
	Start up Time			5		ms			
	Random Jitter	Rj		4	10	ps RMS	1-Sigma		
a	Mechanical Shock						nS duration, ½ sinewave)		
Environmental	Vibration				204 (10 g's				
١Ē	Hermeticity	Per MIL-STD-202, Method 112, (1x10 <sup>-8</sup> atm. cc/s of Helium)							
ē	Thermal Cycle			od 1010	, Condition B	(-55°C to +	125°C, 15 min. dwell, 10 cycles)		
2	Solderability	Per EIAJ-STD-002							
Щ	Soldering Conditions	+260°C max. for 10 secs.							

- Consult factory for availability of higher frequencies.
   See Load circuit diagram #2. Consult factory with nonstandard output load requirements.
   Rise/Fall times are measured between 0.5 V and 2.4 V with TTL load, and between 10% Vdd and 90% Vdd with HCMOS load.

MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.



# MtronPTI Lead Free Solder Profile

